

**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
(Not for submission under 37 CFR 1.99)

Application Number	10001477
Filing Date	2001-11-01
First Named Inventor	Steve Roe
Art Unit	2123
Examiner Name	Jason Scott Proctor
Attorney Docket Number	CD01203M

/JP/	1	USPTO Notice of Allowance for Application Number 10/001,477 (CD01203M) dated 12/04/2009; 7 pages	<input type="checkbox"/>
/JP/	2	USPTO Notice of Allowance for Application Number 09/989,765 (CD01194M) dated 12/022/2009; 8 pages	<input type="checkbox"/>
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/JP/	12	USPTO Non-Final Rejection for Application Number 11/201,627 (CD04109) dated 12/24/2009; 22 pages	<input type="checkbox"/>
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Examiner Signature	/Jason Proctor/	Date Considered	03/05/2010
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/JP/	1	Adham et al., "Preliminary Outline of the IEEE P1500 Scalable Architecture for Testing Embedded Cores," 1999, IEEE; 6 pages	<input type="checkbox"/>
/JP/	2	Page Maintained by Efsthathiou, "Analog Electronics: Basic Circuits of Operational Amplifiers," < <a href="http://web.archive.org/web/20030306113442/web/20030306113442">http://web.archive.org/web/20030306113442</a> > <del>December 31, 2002, version</del> , retrieved from the Internet Archives; 10 pages <b>December 2, 2008</b>	<input type="checkbox"/>

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